

Attorney Docket No. 42P10807

03/34/04

#7 2127

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Erik Cota-Robles et al.

Examiner: Lillian Vo

Application No. 09/931,072

Art Unit: 2127

Filed: August 15, 2001

For: **TRACKING OPERATION
SYSTEM PROCESS AND
THREAD EXECUTION AND
VIRTUAL MACHINE
EXECUTION IN HARDWARE
OR IN A VIRTUAL MACHINE
MONITOR**

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Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

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Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered, and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.	
By: <u>Anne Collette</u> Anne Collette	Date: <u>3/10/2004</u>

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

This Information Disclosure Statement is being submitted pursuant to one of the following (as indicated by an "X" to the left of the appropriate paragraph):

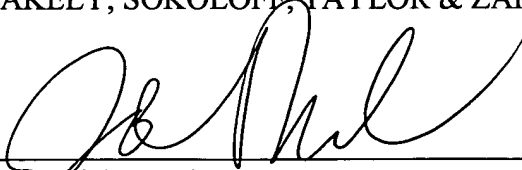
- X 37 C.F.R. §1.97(b).
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 3/10/04



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Substitute Form 1449/PTO (Modified) <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center; font-size: small;">(use as many sheets as necessary)</p>				Complete if Known	
		Application Number		09/931,072	
		Filing Date		August 15, 2001	
		First Named Inventor:		Erik Cota-Robles	
		Group Art Unit		2127	
		Examiner Name		Lilian Vo	
		Attorney Docket Number		42P10807	
Sheet 1 of 1					

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No.	U.S. Patent Document Number ¹ Kind Code ² (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		5,582,717	Di Santo	12-10-1996		
		6,282,650	Davis	08-28-2001		

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. ¹	Foreign Patent Document ² Office ³ Number ⁴ Kind Code ⁵ (If known)			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		DE	4217444		Toyohisa et al.	12-03-1992		
		EP	0473913		Farrell	03-11-1992		
		WO	9729567		Gressel et al.	08-14-1997		
		WO	0127723		Vamvakas et al.	04-19-2001		
		WO	0127821		Chen et al.	04-19-2001		

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OTHER ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995), Chapter 3 ✓	
		DAVIDA, GEORGE I. et al., "Defending Systems Against Viruses through Cryptographic Authentication", Proceedings of the Symposium on Security and Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2, (May 1989) ✓	
		INTEL, "IA-32 Intel Architecture Software Developer's Manual Citation: Volume 3: System Programming Guide Miscellaneous Information", (2003), pp. 13-1 through 13-24 ✓	
		KASHIWAGI, KAZUHIKO et al., "Design and Implementation of Dynamically Reconstructing System Software", Software Engineering Conference, Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8, (1996) ✓	
		LUKE, JAHN et al., "Replacement Strategy for Aging Avionics Computers", IEEE AES Systems Magazine, XP002190614, (March 1999)	
		MENEZES, OORSCHOT, "Handbook of Applied Cryptography", CRC Press LLC, USA XP002201307, (1997), p. 475 ✓	
		RICHT, STEFAN et al., "In-Circuit-Emulator Wird Echtzeitauglich", Elektronik, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620, (August 6, 1991), pp. 100-103	
		SAEZ, SERGIO et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615, (July 1999), pp. 43-48	

Examiner Signature	Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.